

FIG. 1

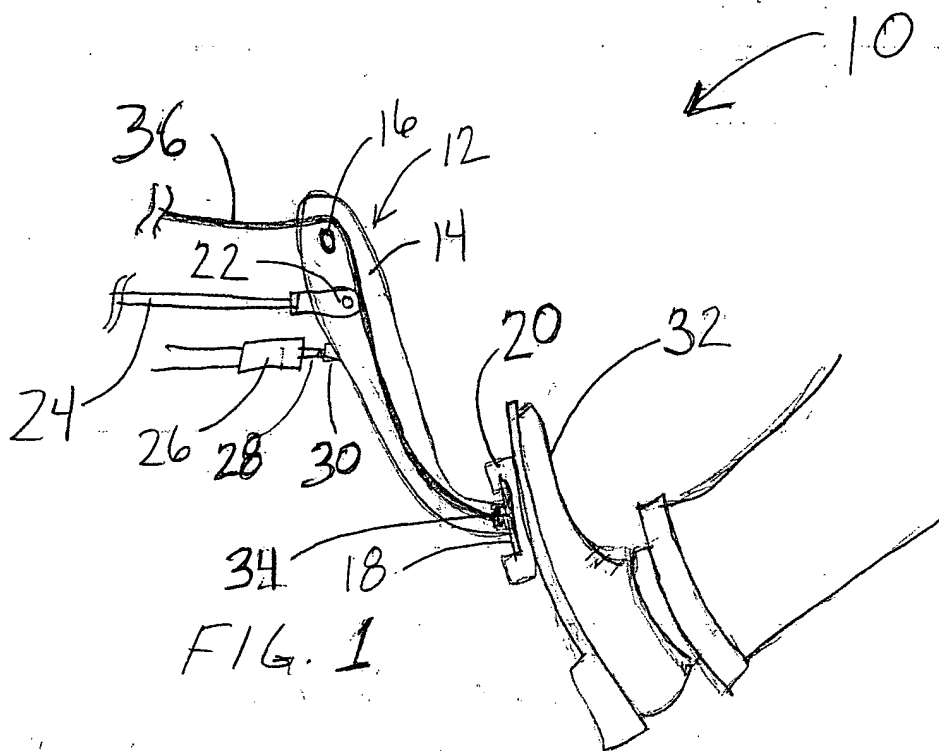


FIG. 1

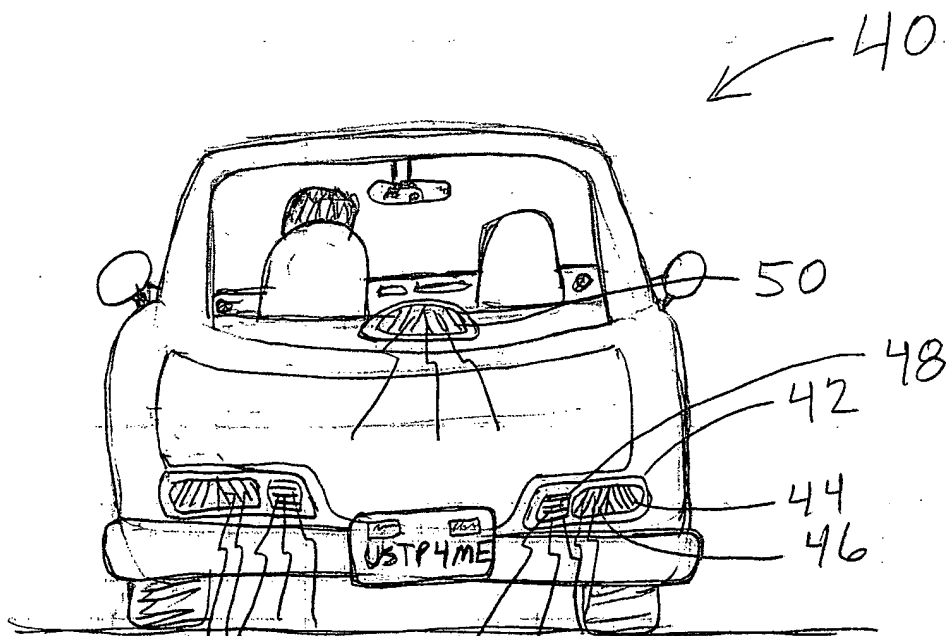


FIG. 2

FIG. 3

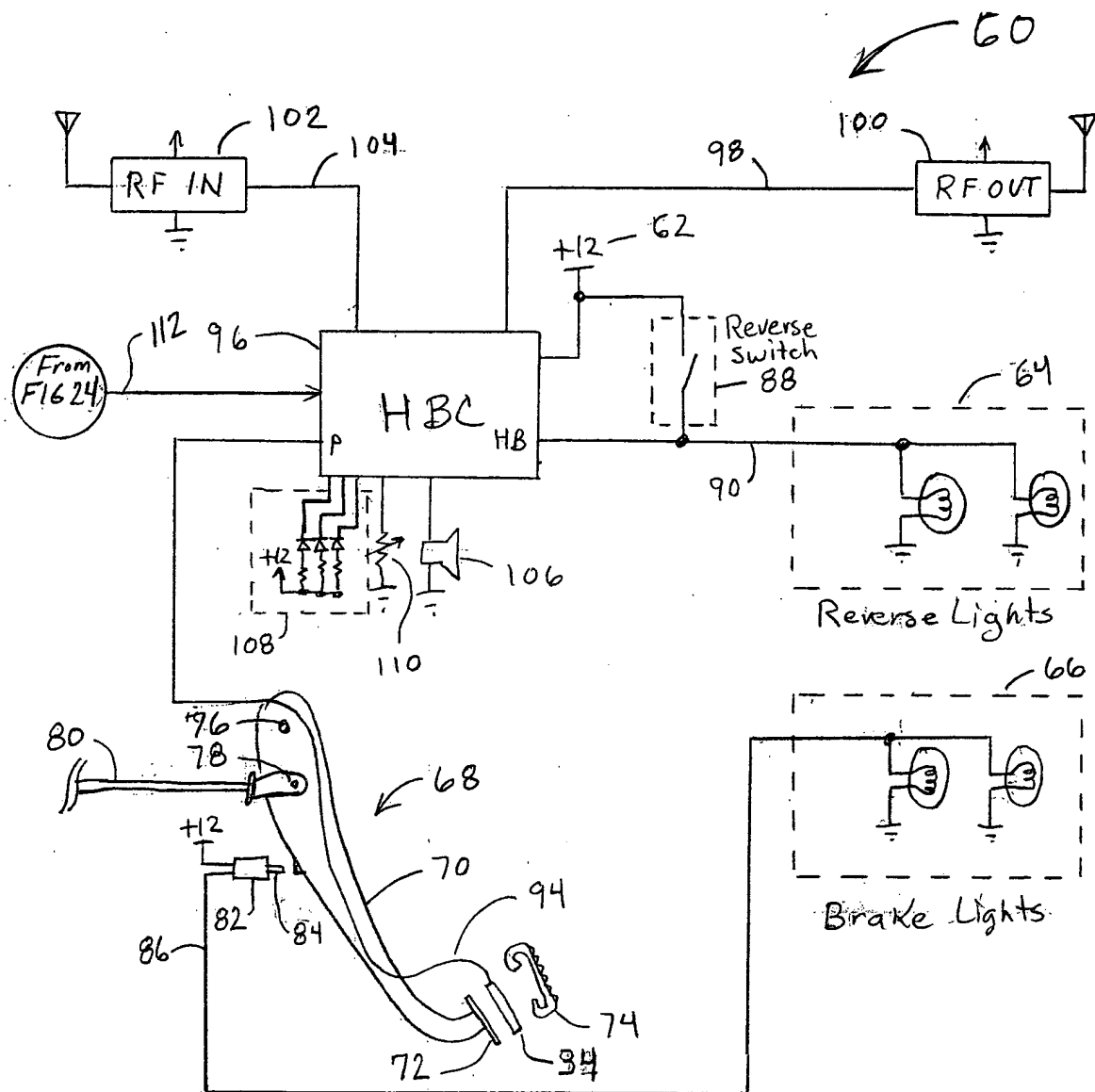


FIG. 3

FIG. 4

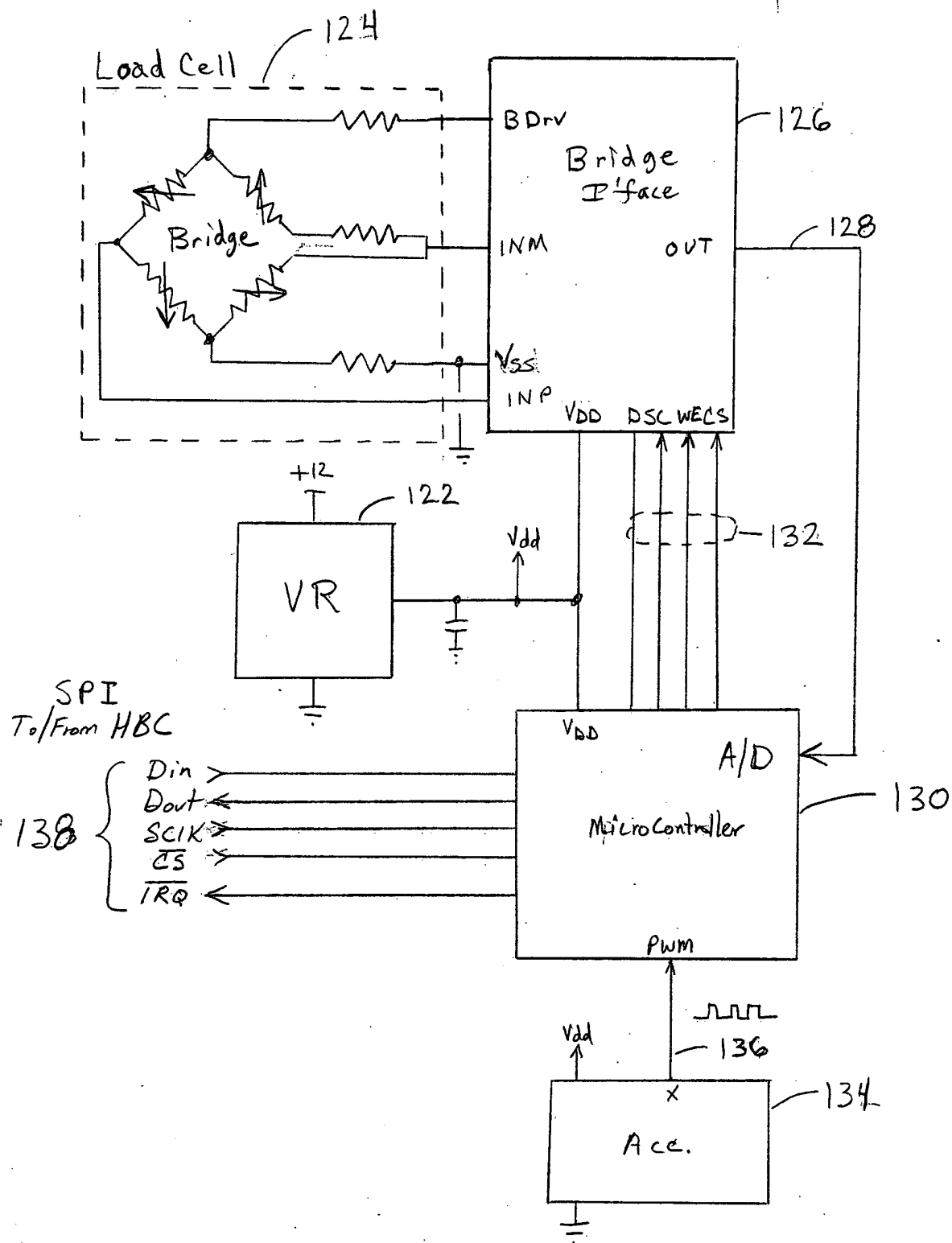


FIG. 4

FIG. 5

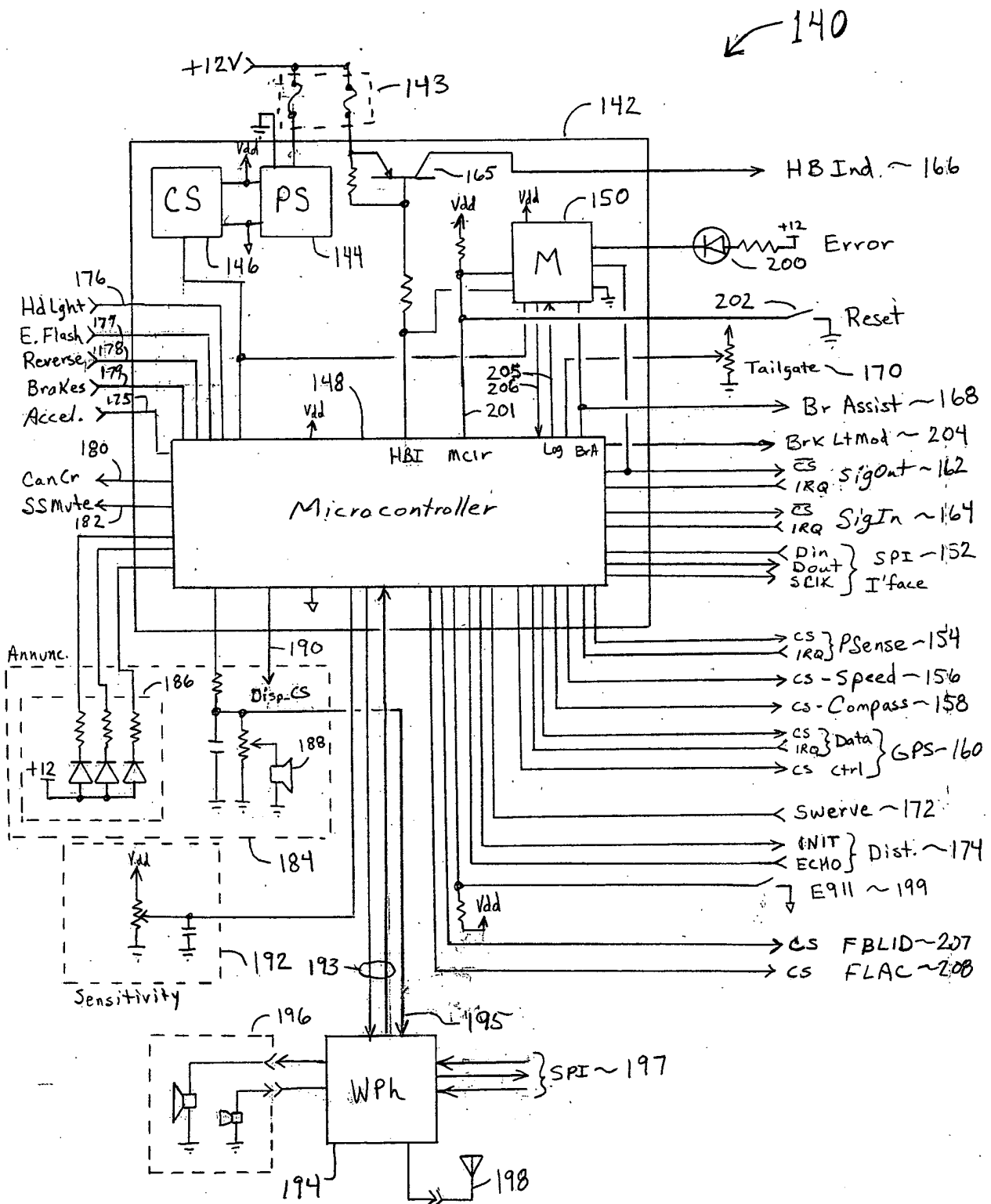


FIG. 5

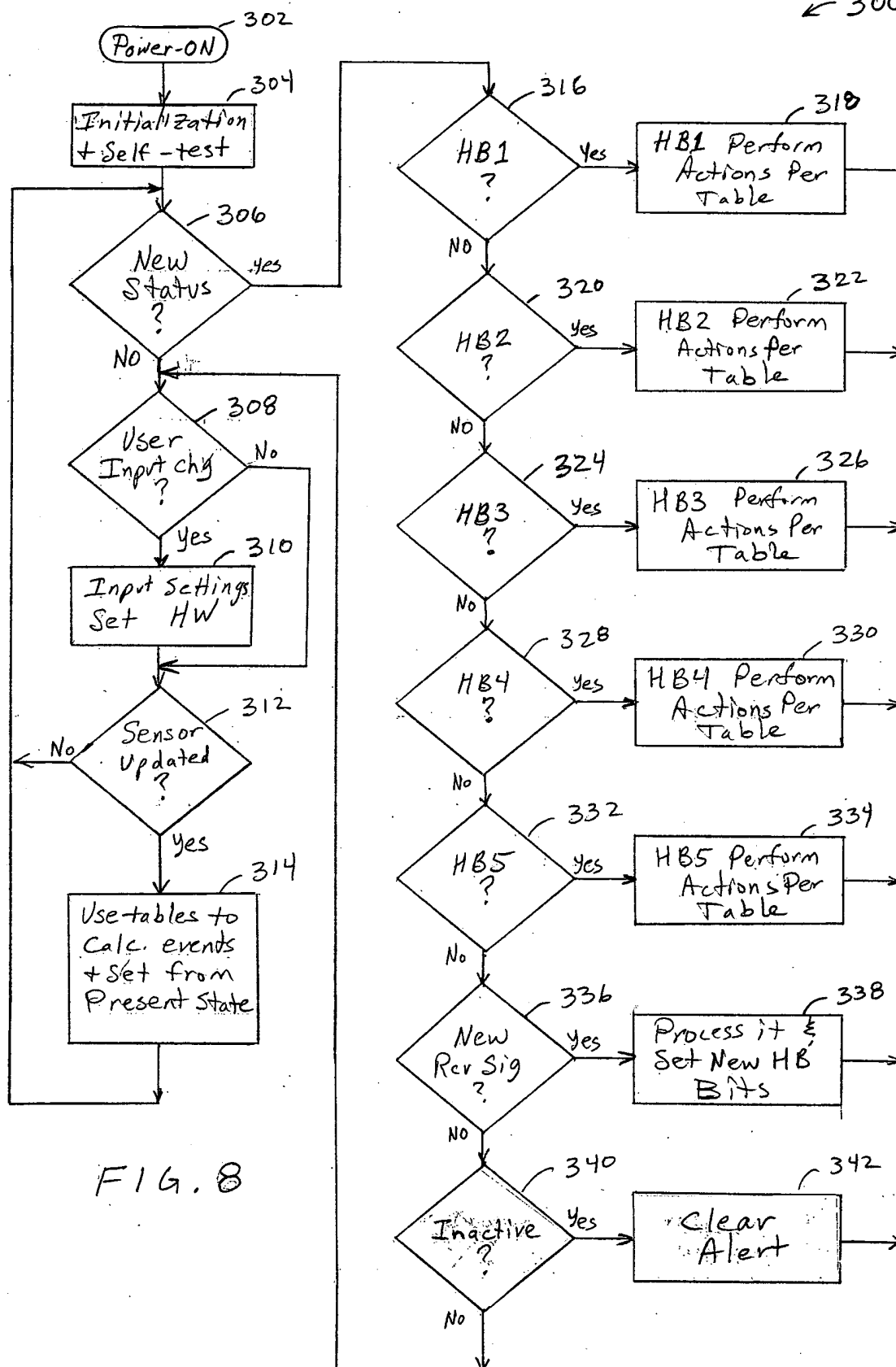
The diagram illustrates a packet structure with the following components:

- Regeneration Levels (212):** A horizontal bar divided into five segments labeled 1, 2, 3, 4, and 5. Reference numerals 214, 216, 218, 220, and 222 point to the boundaries between these segments.
- Regeneration Channels (224):** A horizontal bar divided into 16 segments labeled 8, 9, 10, 11, 12, 13, 14, 15, 0, 1, 2, 3, 4, 5, 6, and 7. Reference numerals 226 and 228 point to the boundaries between segments 12 and 13, and between segments 5 and 6, respectively.
- Slot (230):** A horizontal bar divided into four segments labeled E, R, U, and an empty segment. Reference numerals 232 and 234 point to the boundaries between segments E and R, and R and U, respectively.
- Packet (236):** A horizontal bar at the bottom, currently empty.

The diagram shows a digital signal waveform. It starts with a low-level signal, followed by a high pulse, then a low pulse, then another high pulse. After this, there is a horizontal line representing a low-level signal. Two vertical dashed lines are drawn on this line, with a double-headed arrow between them labeled "Var. Delay". Following the second dashed line, the signal transitions to a high pulse, then a low pulse, then another high pulse, and finally a long high pulse. A break symbol (two parallel slanted lines) is shown on the long high pulse. In the top right corner, there is a handwritten "240" with an arrow pointing towards the signal.

FIG. 7

← 300



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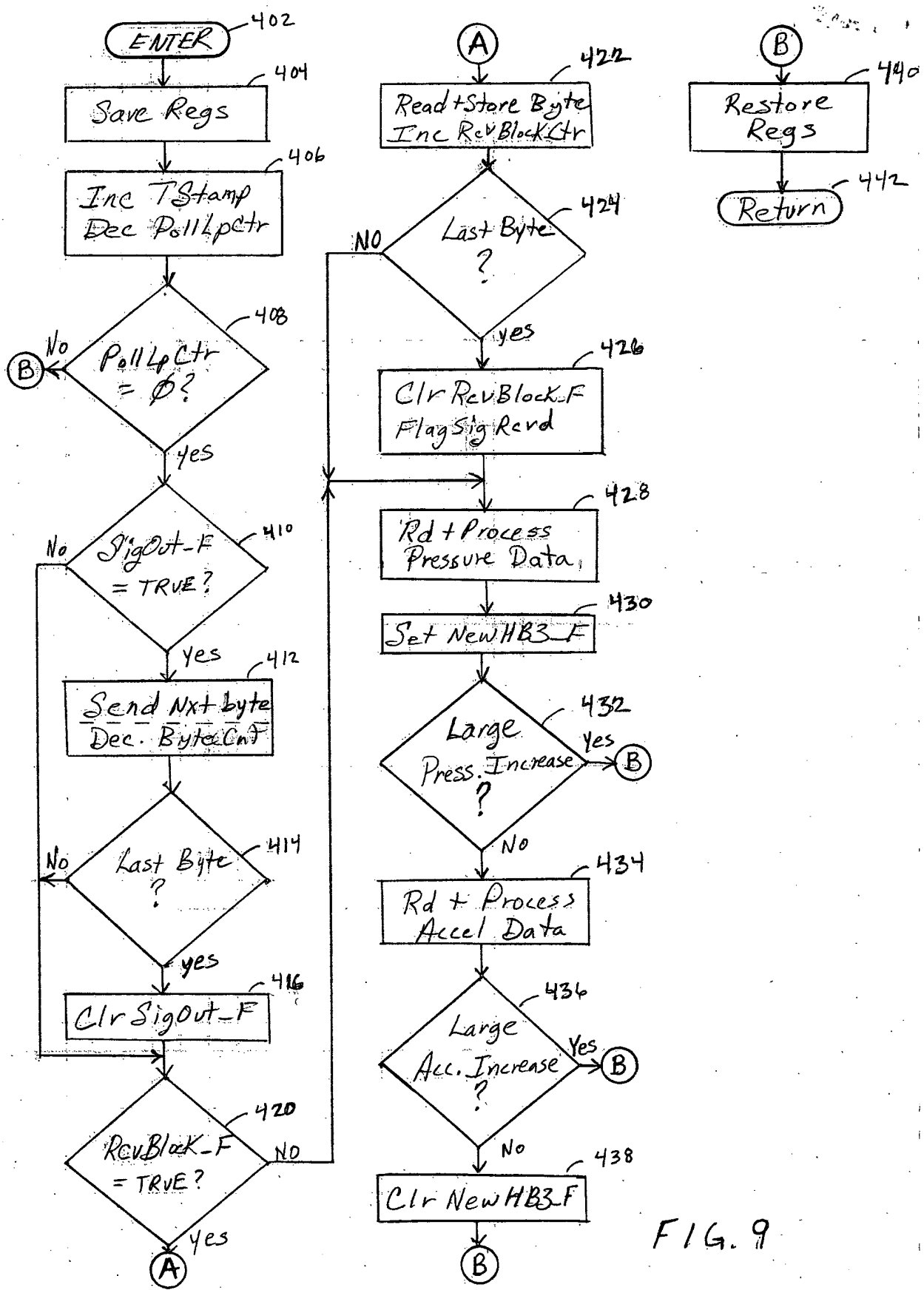
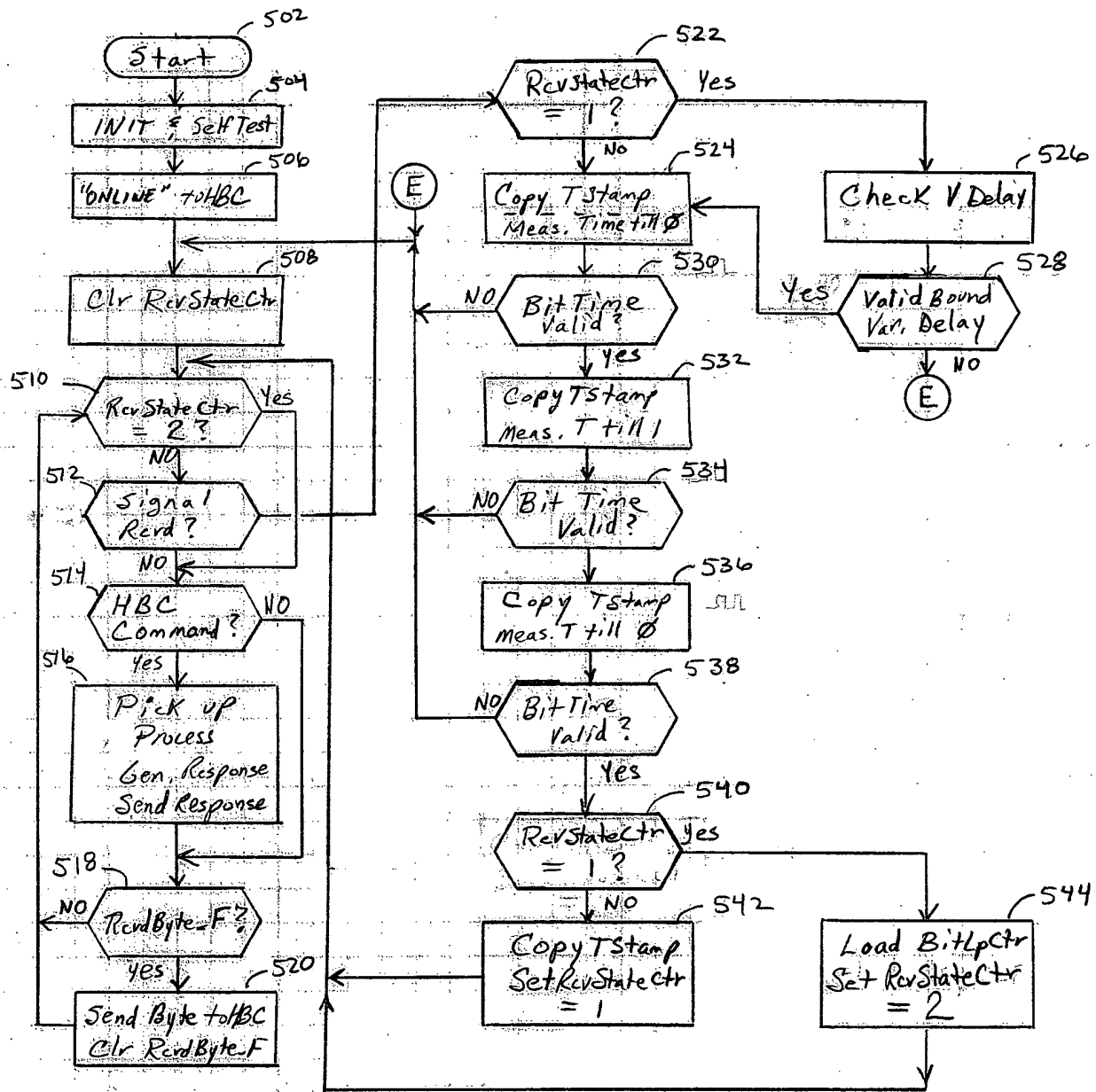


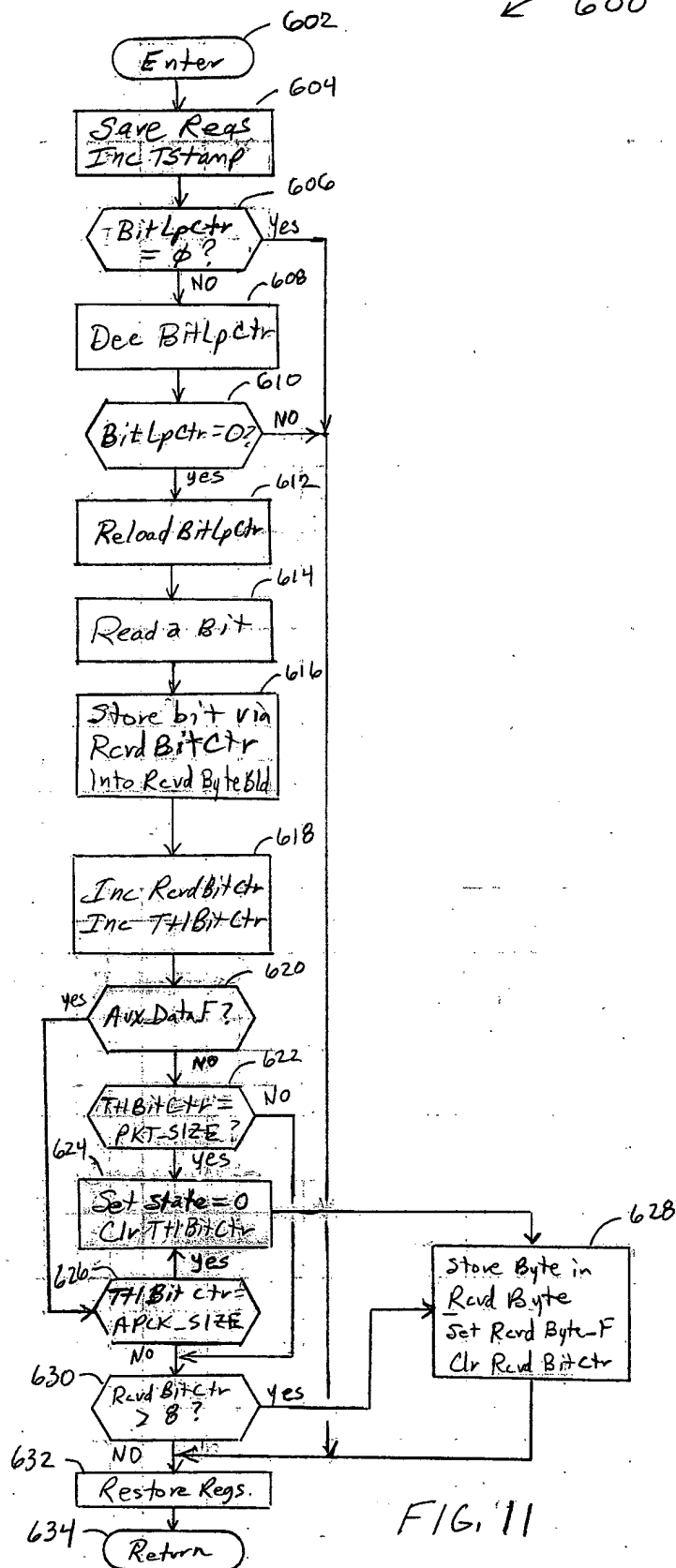
FIG. 9

← 500





← 600



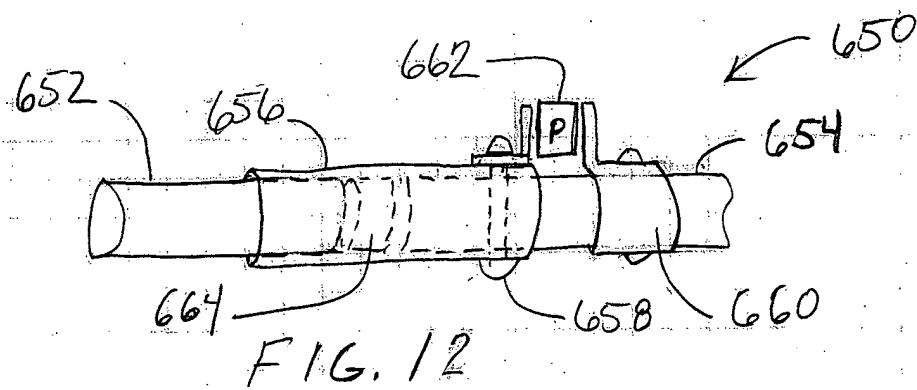
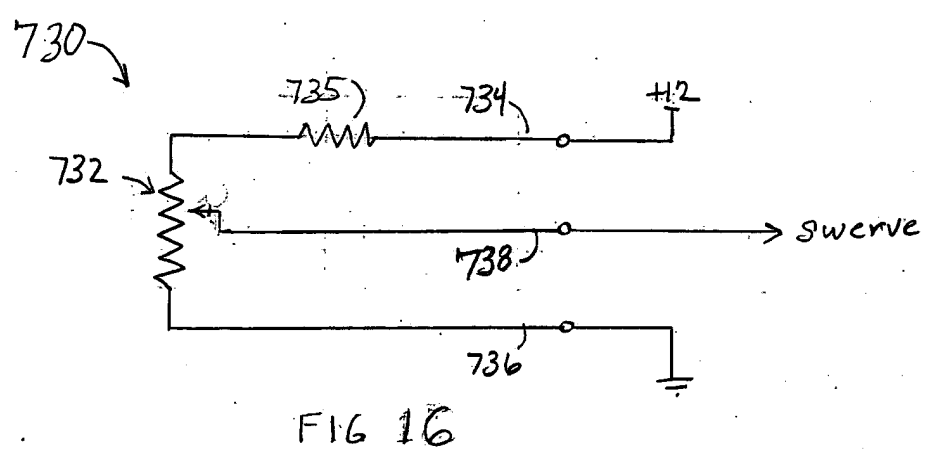
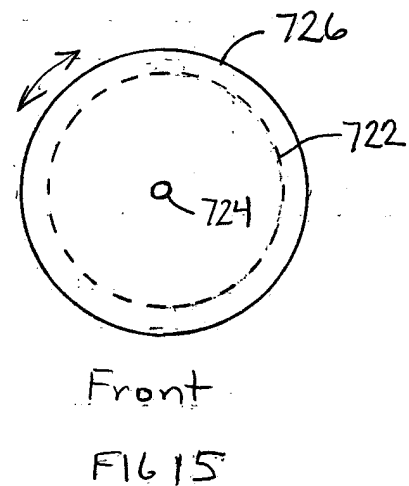
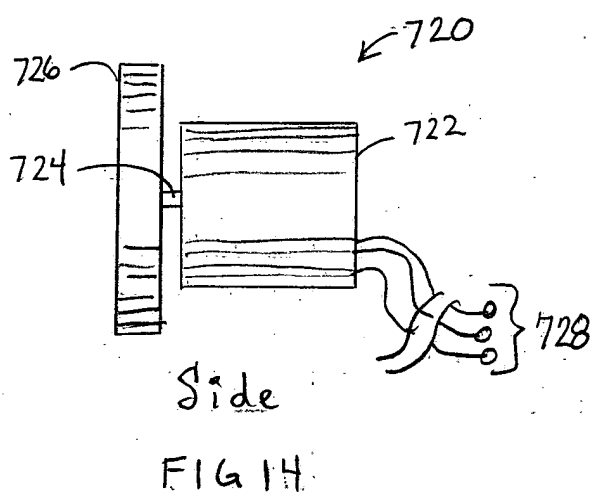
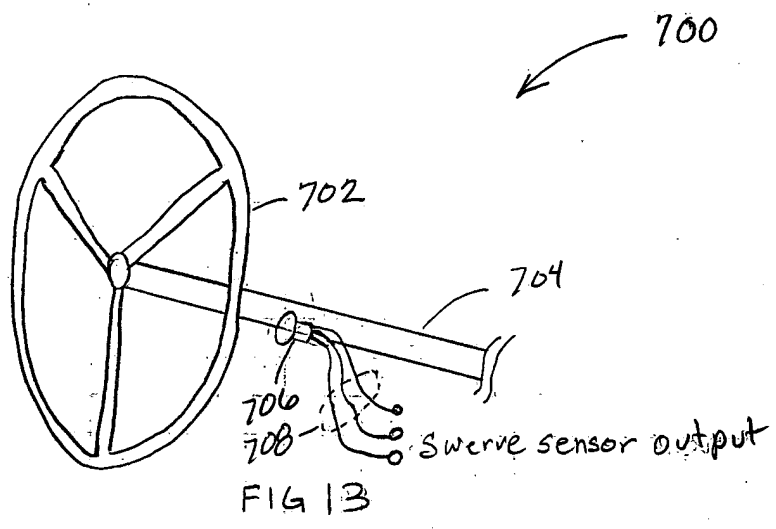


FIG. 13



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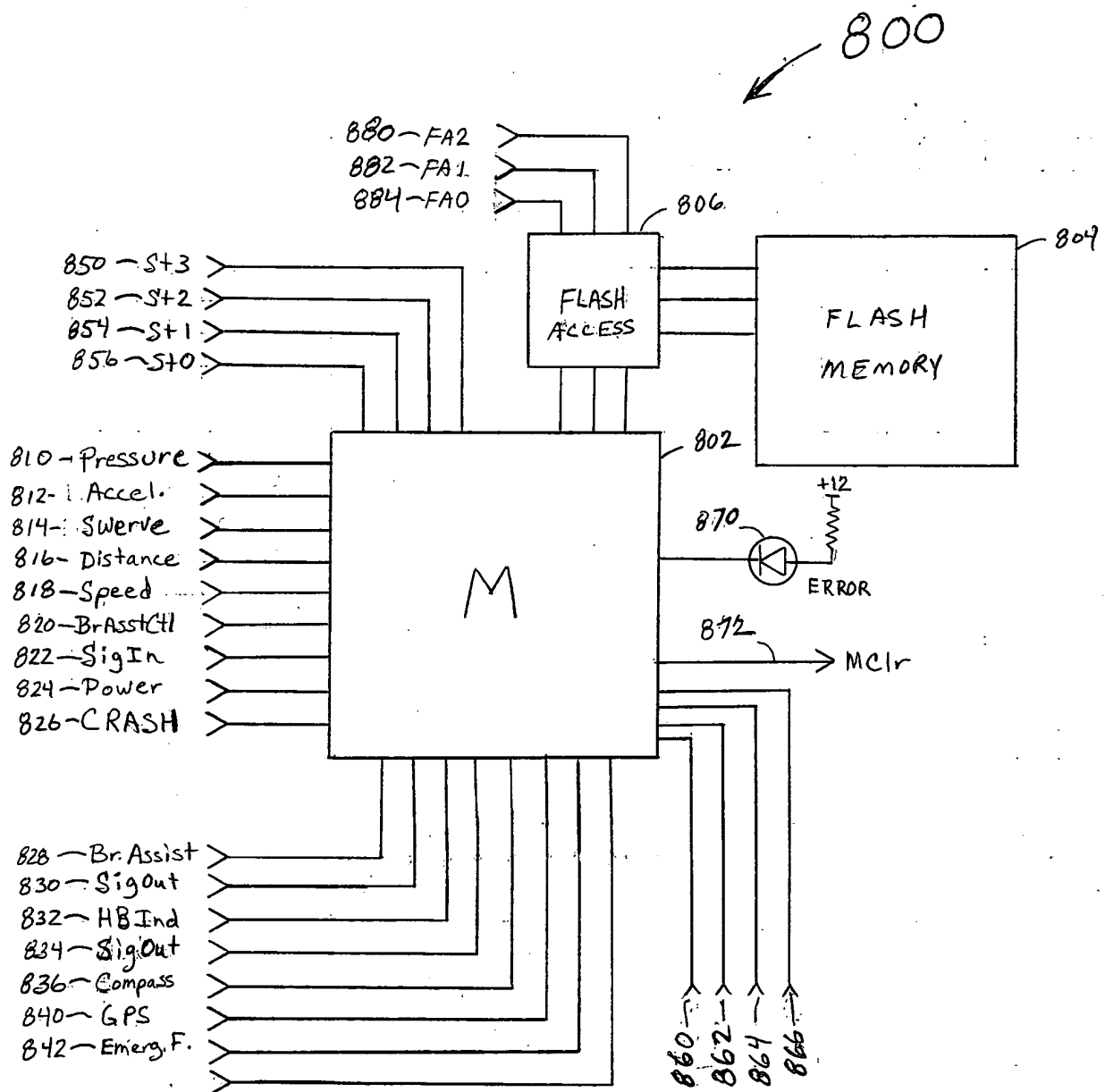


FIG. 17

FIG. 18

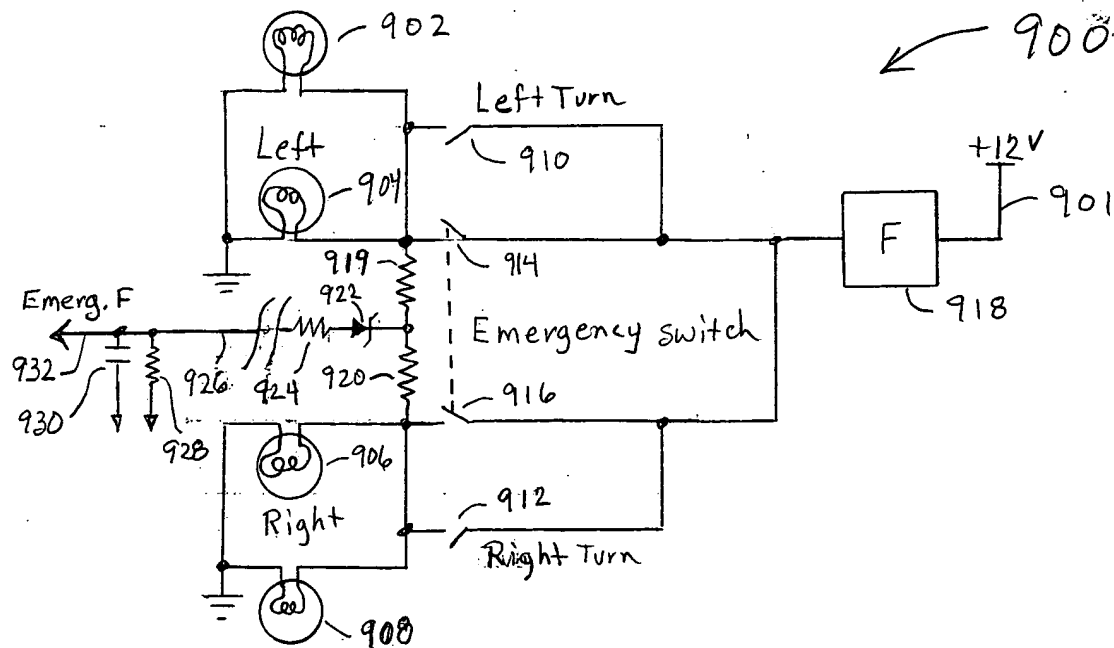


FIG. 18  
Emergency Flasher Sense

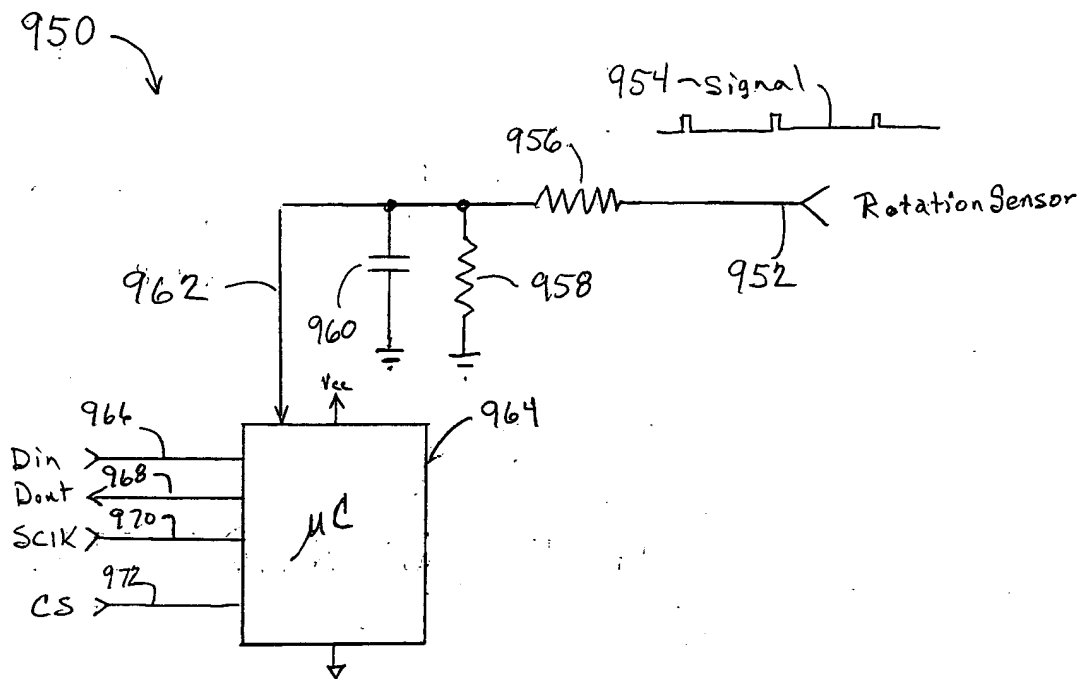


FIG. 19

FIG. 20

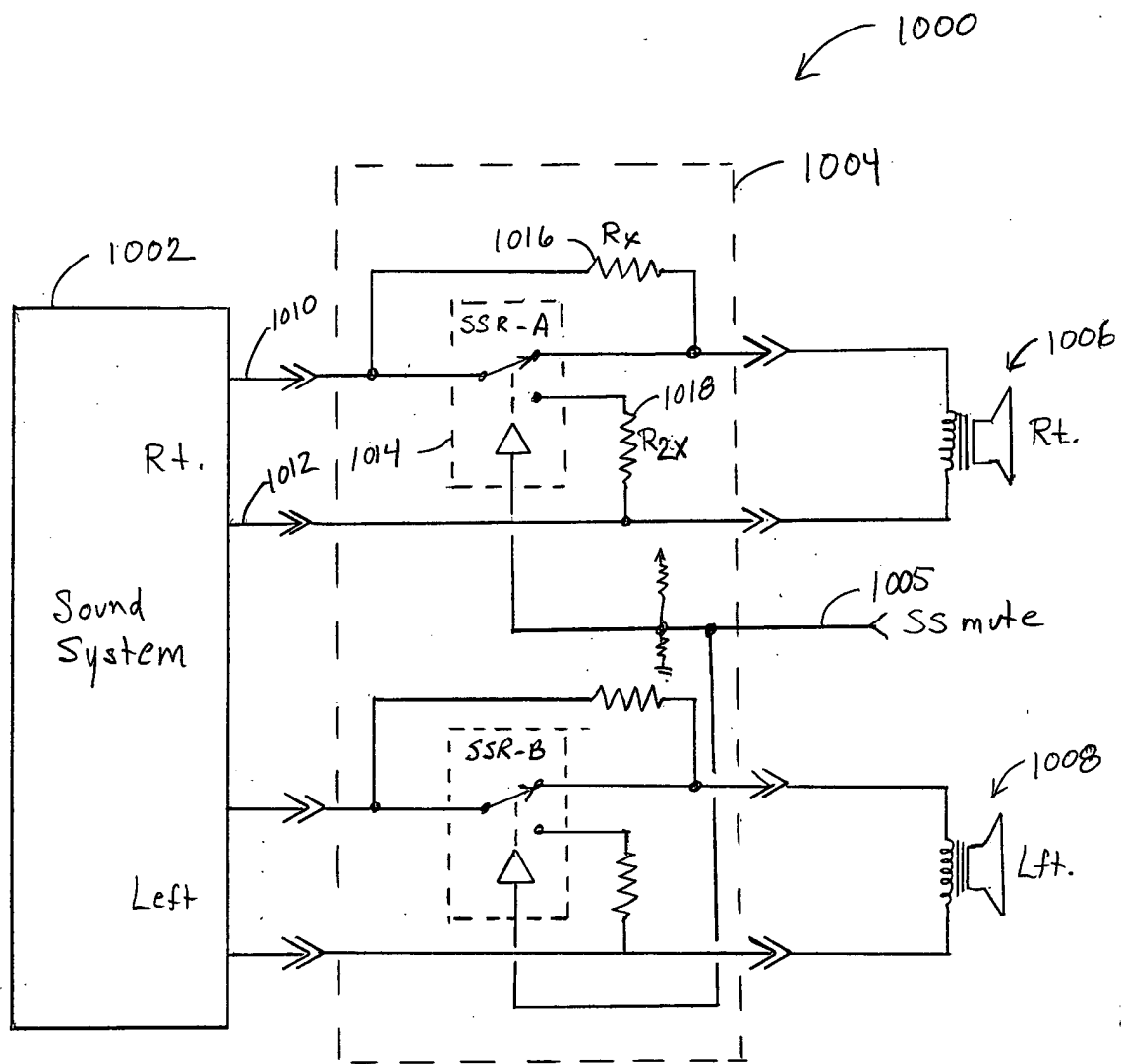


FIG. 20

09730327-033001

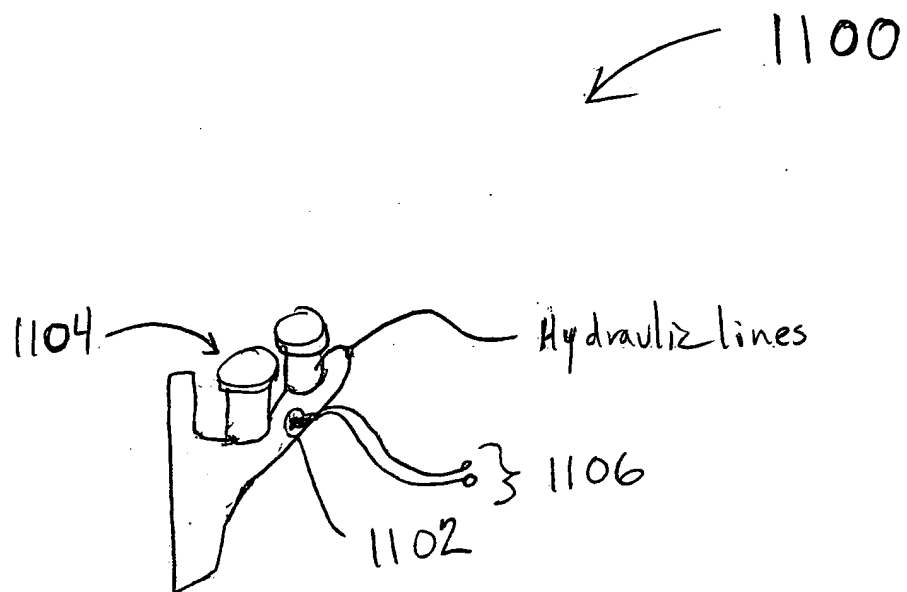


FIG. 21

09730327-033001

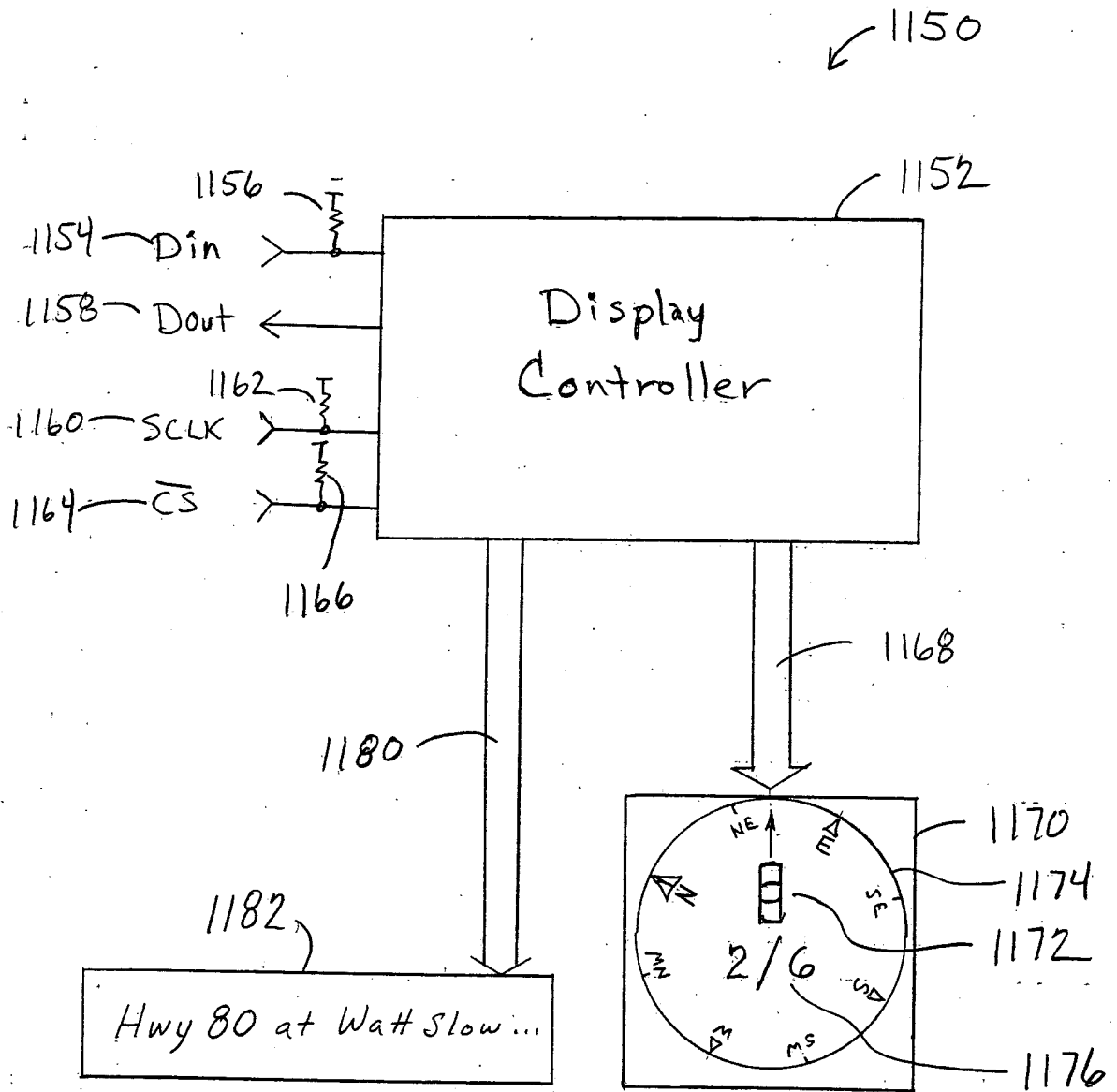


FIG. 22



FIG. 23

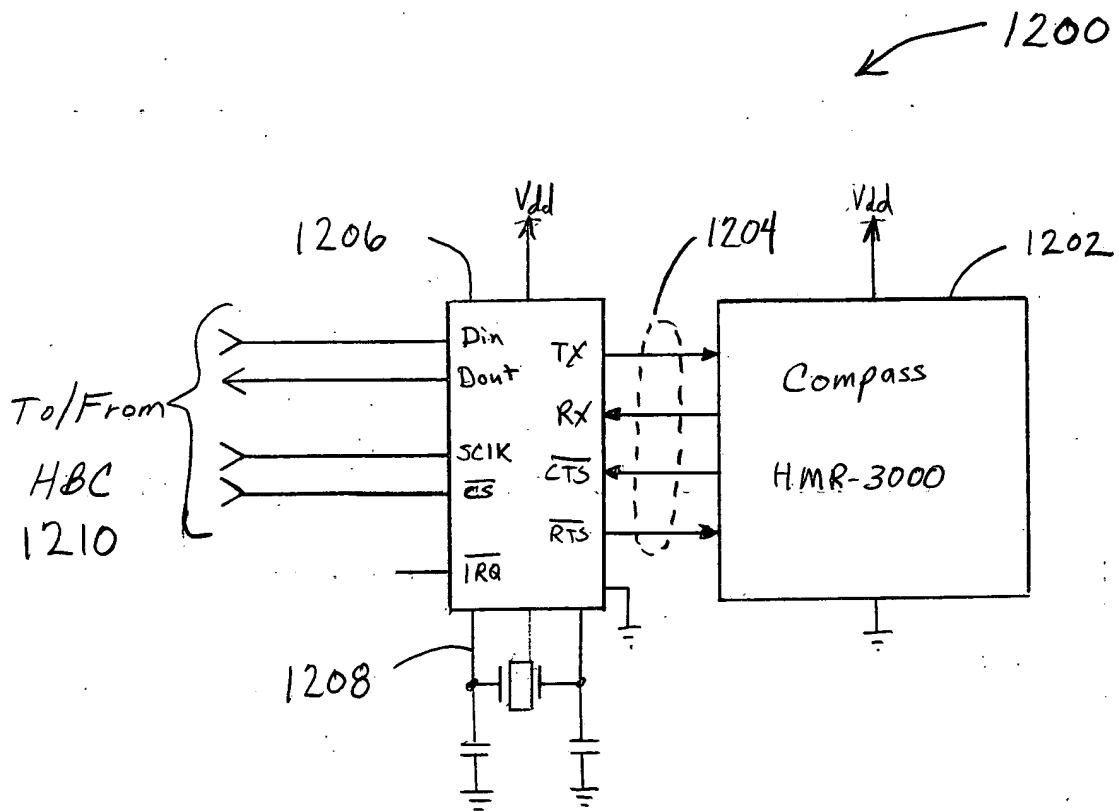


FIG. 23

09730387-033001

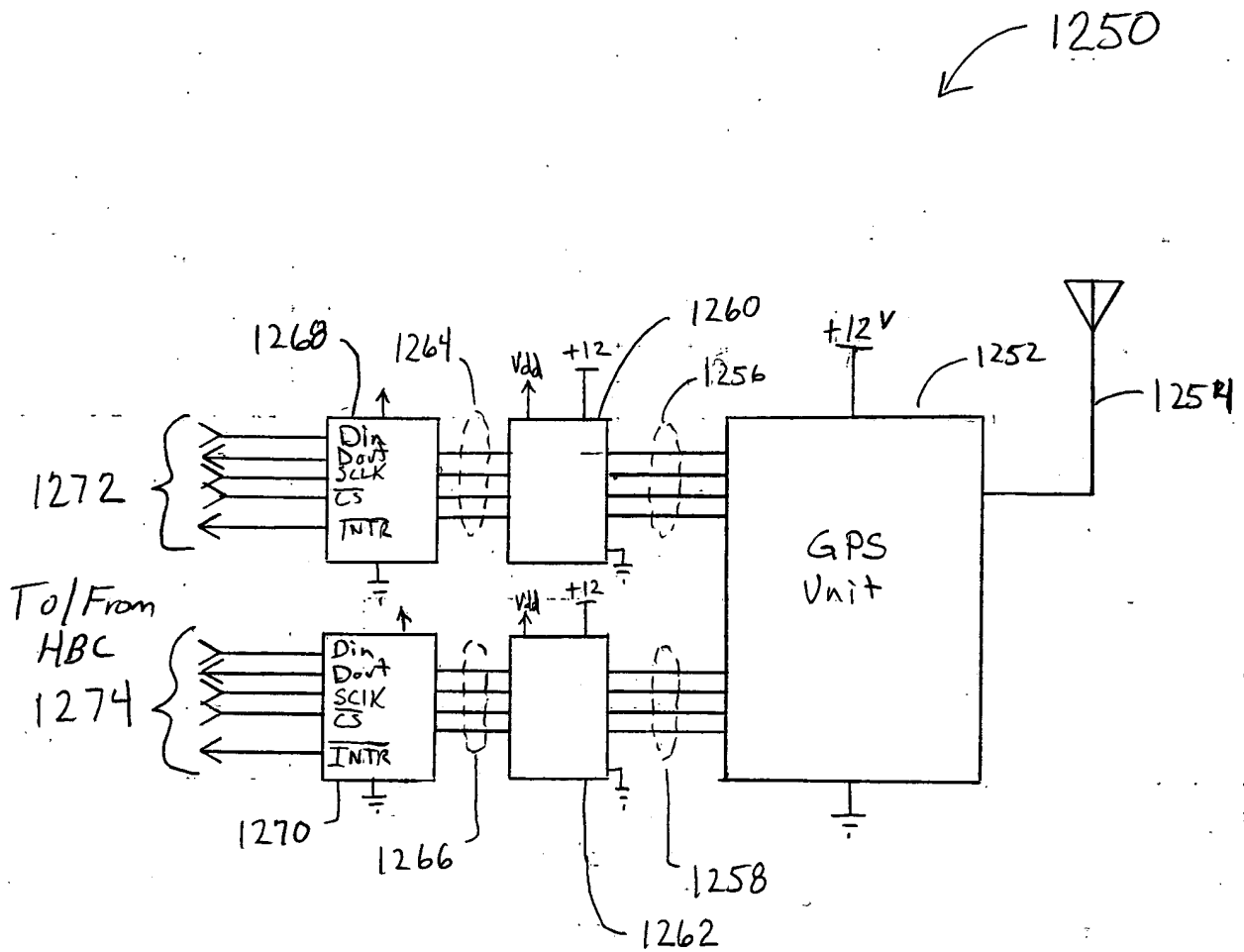


FIG. 24

09730327.033001

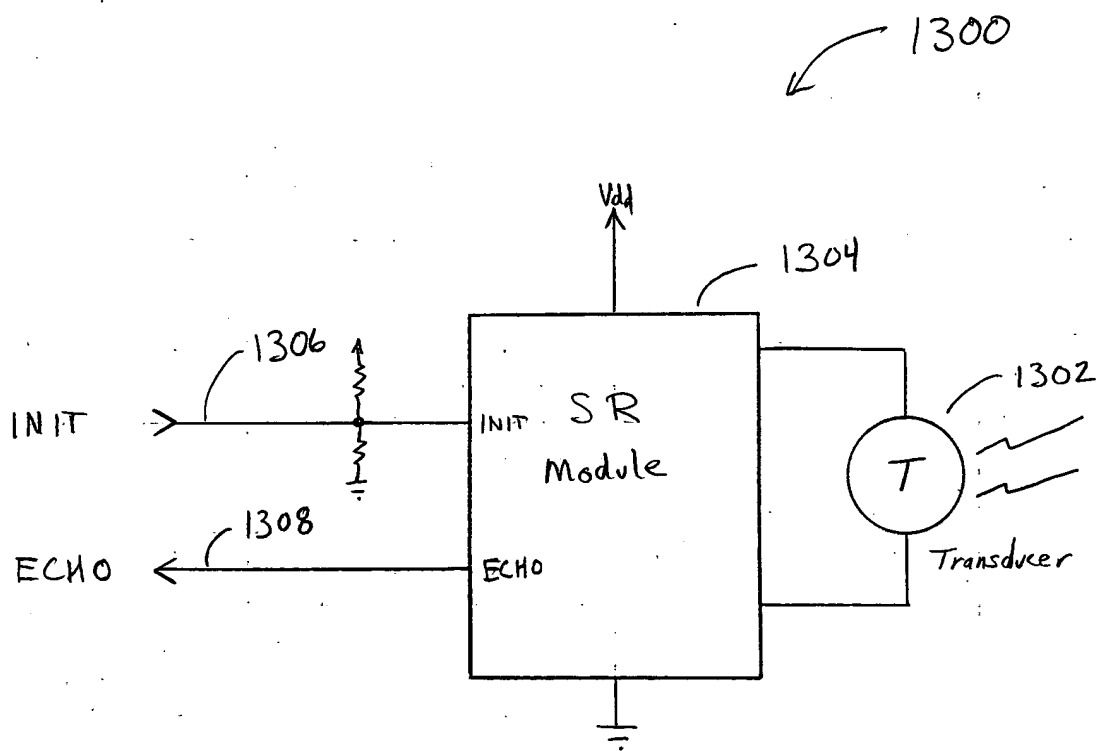


FIG. 25

09730327.033001

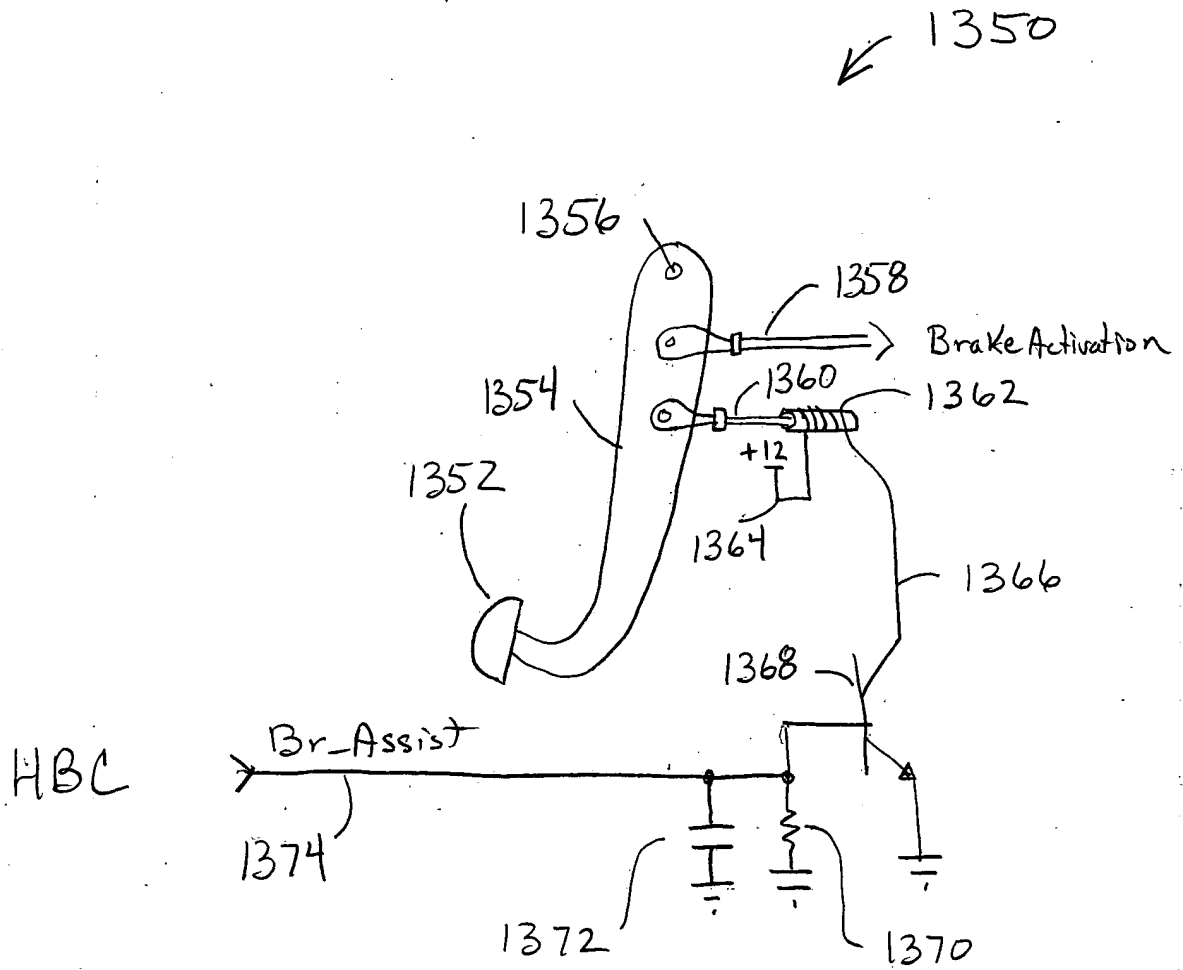


FIG. 26

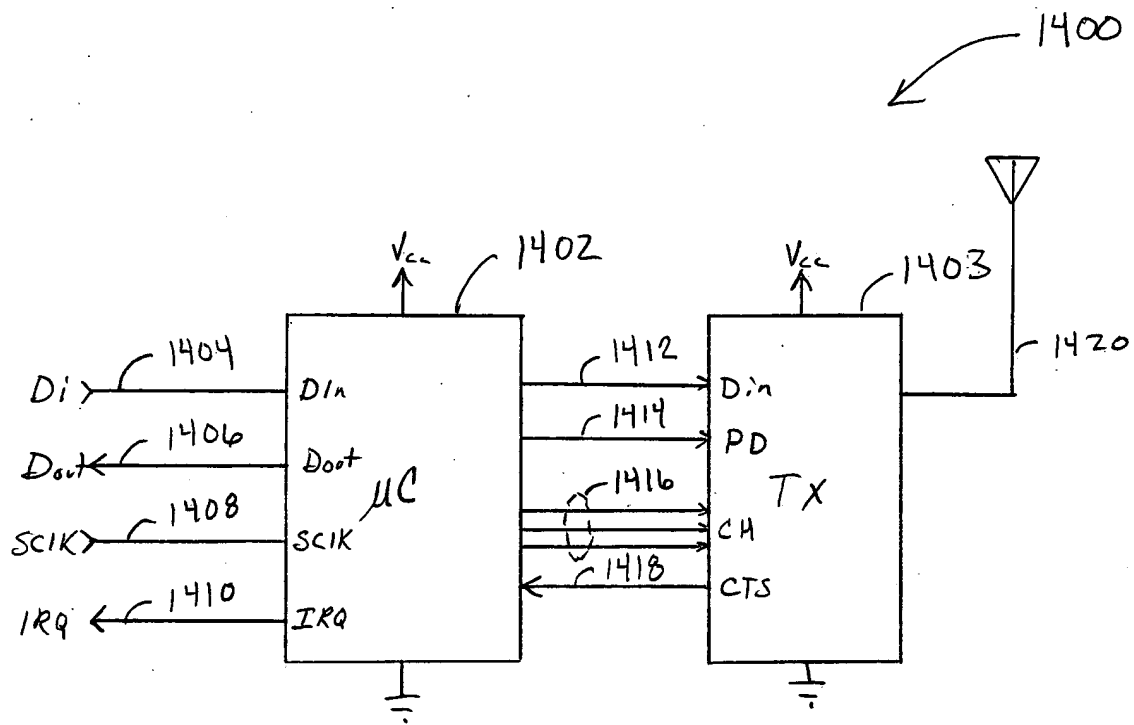


FIG. 27

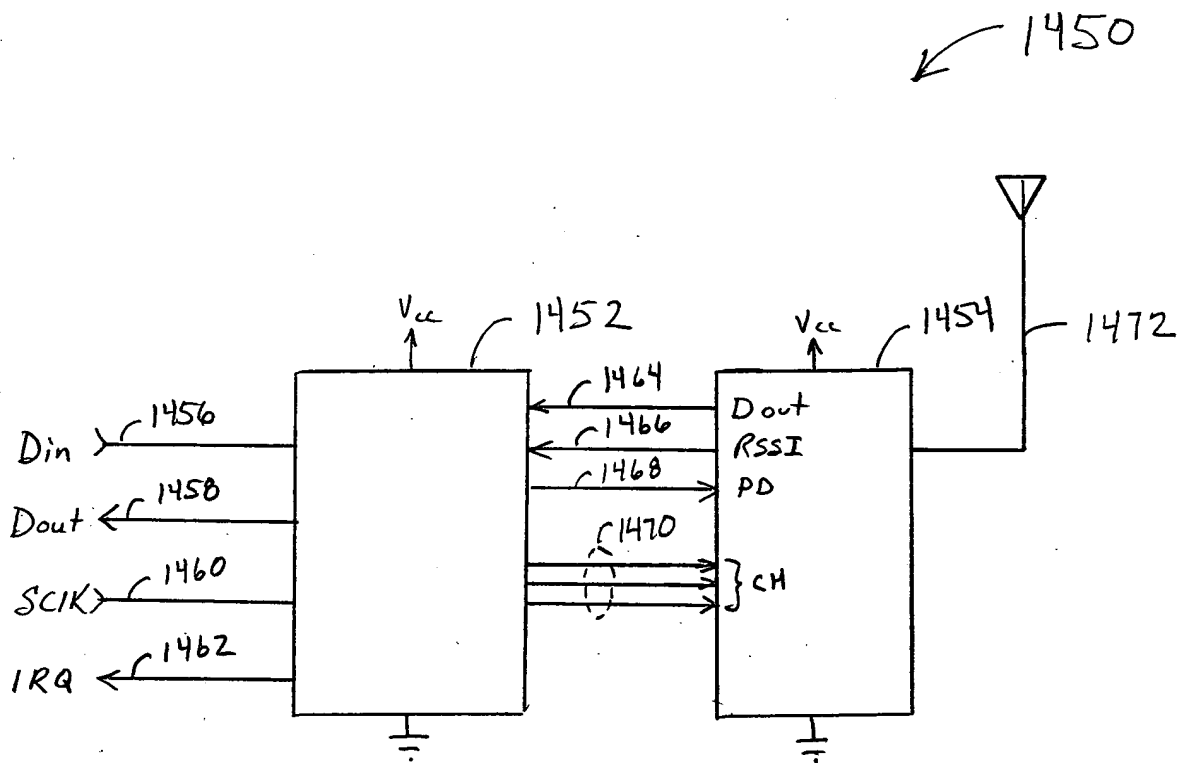


FIG. 28

FIG. 29

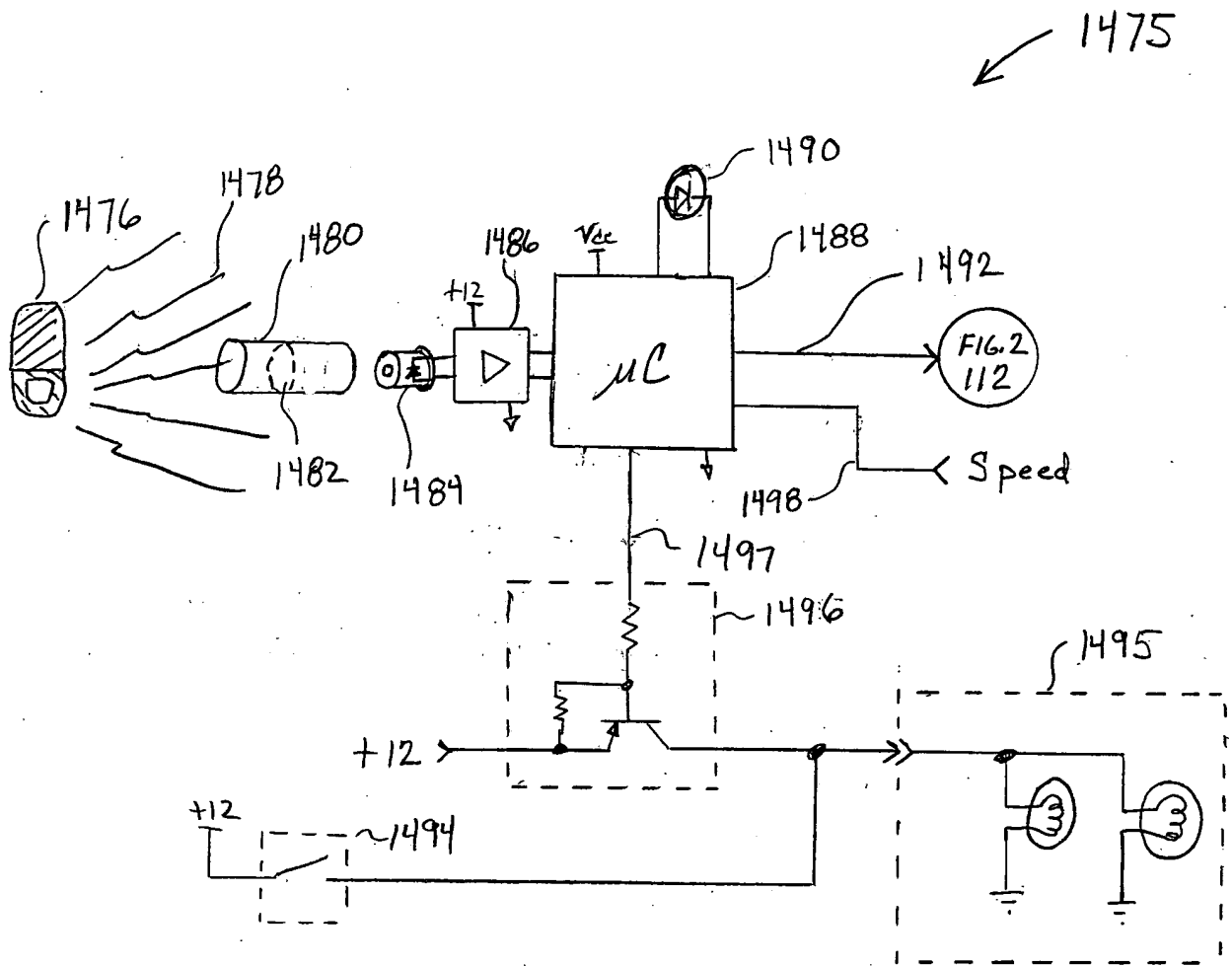


FIG. 29

FIG. 30

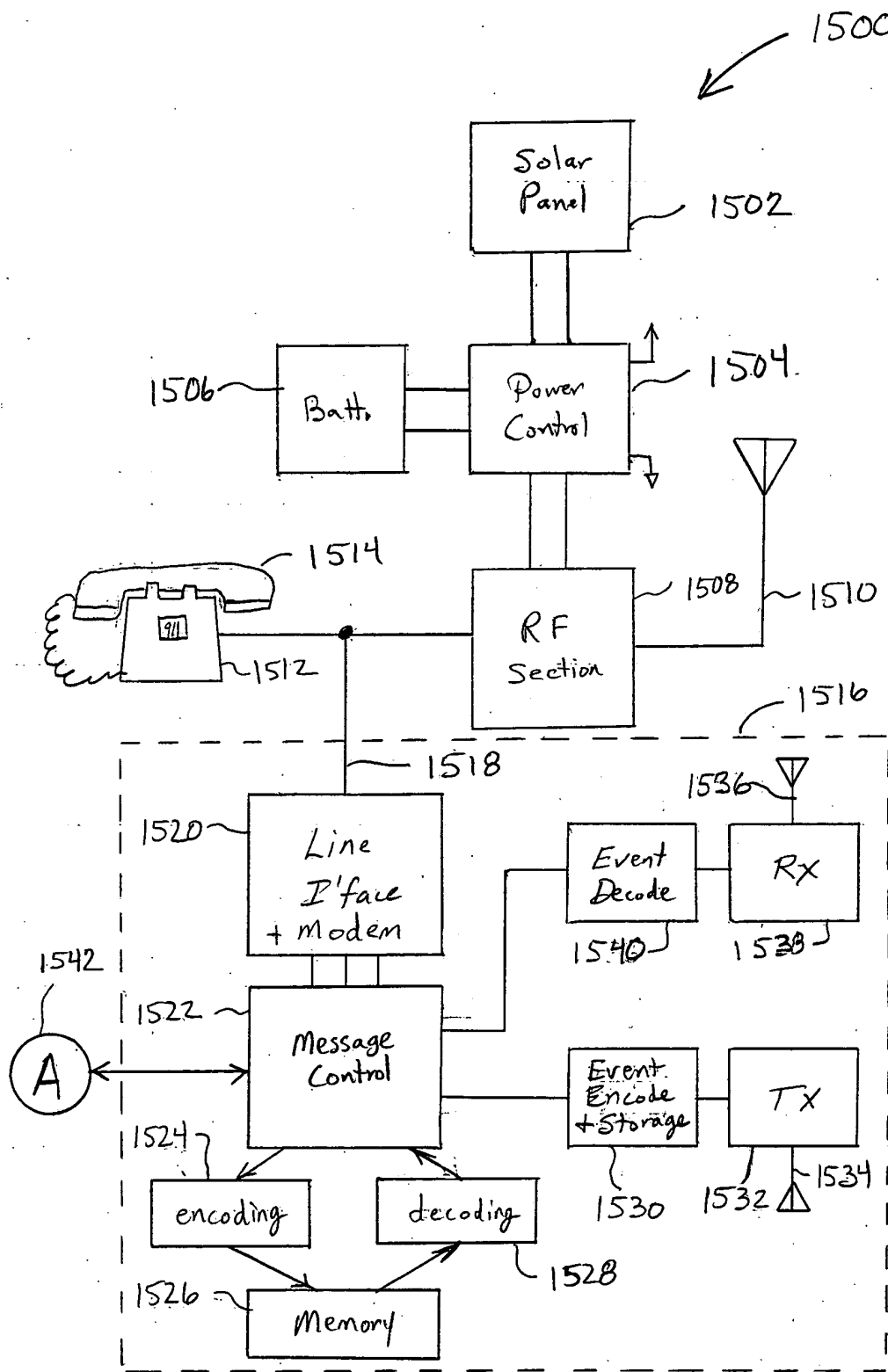


FIG. 30



FIG. 31

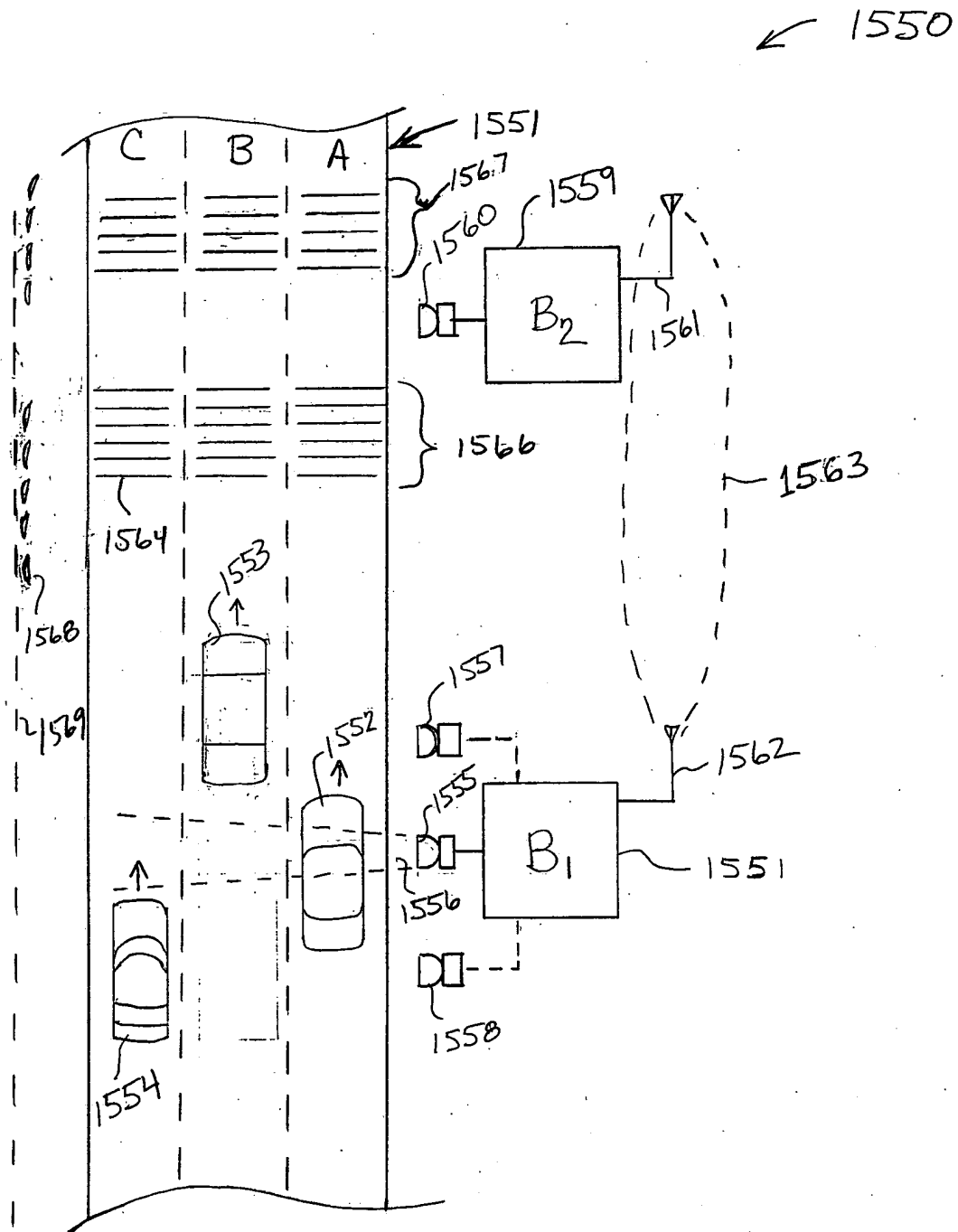


FIG. 31

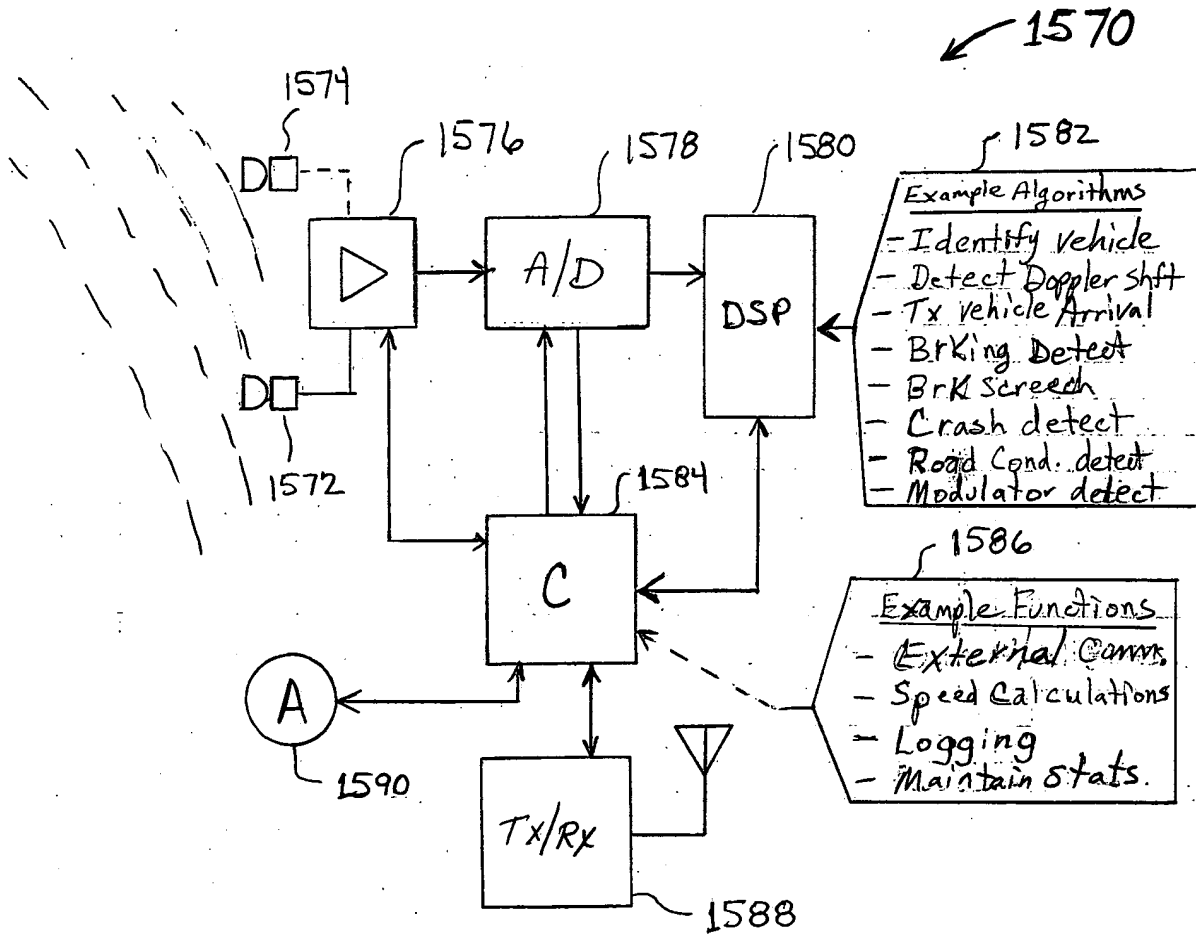


FIG. 32

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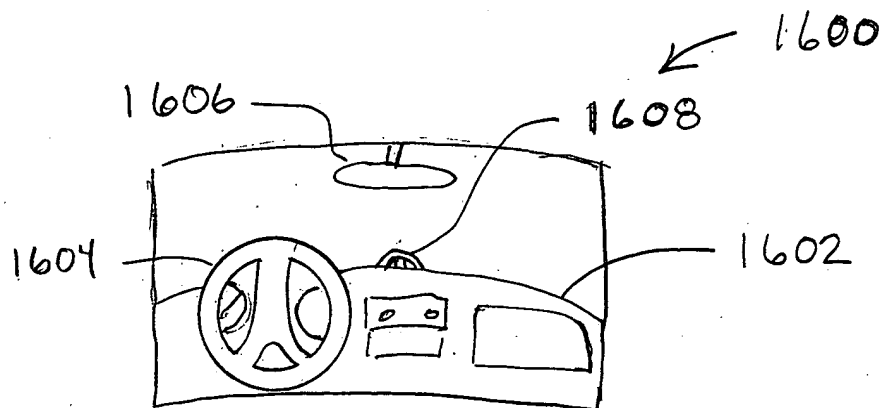


FIG. 33  
Driver Visual Alert Mounting

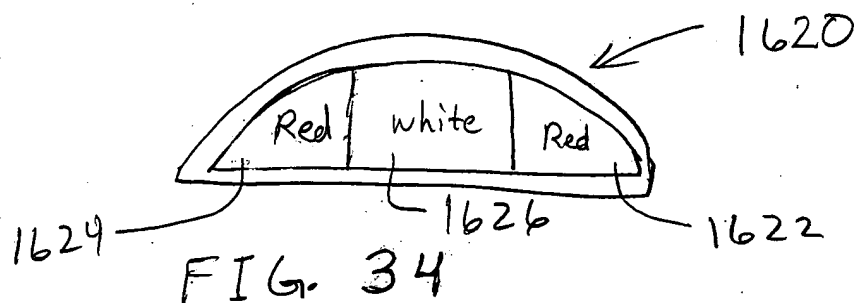


FIG. 34

FIG. 35

1650

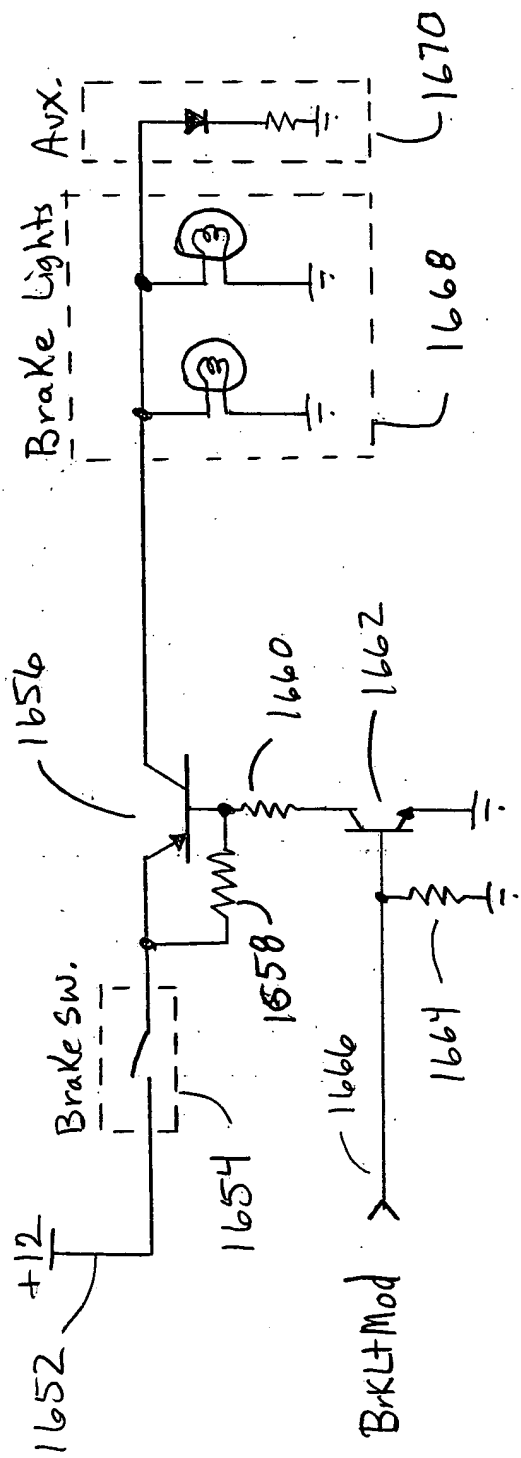


FIG. 35

FIG. 36

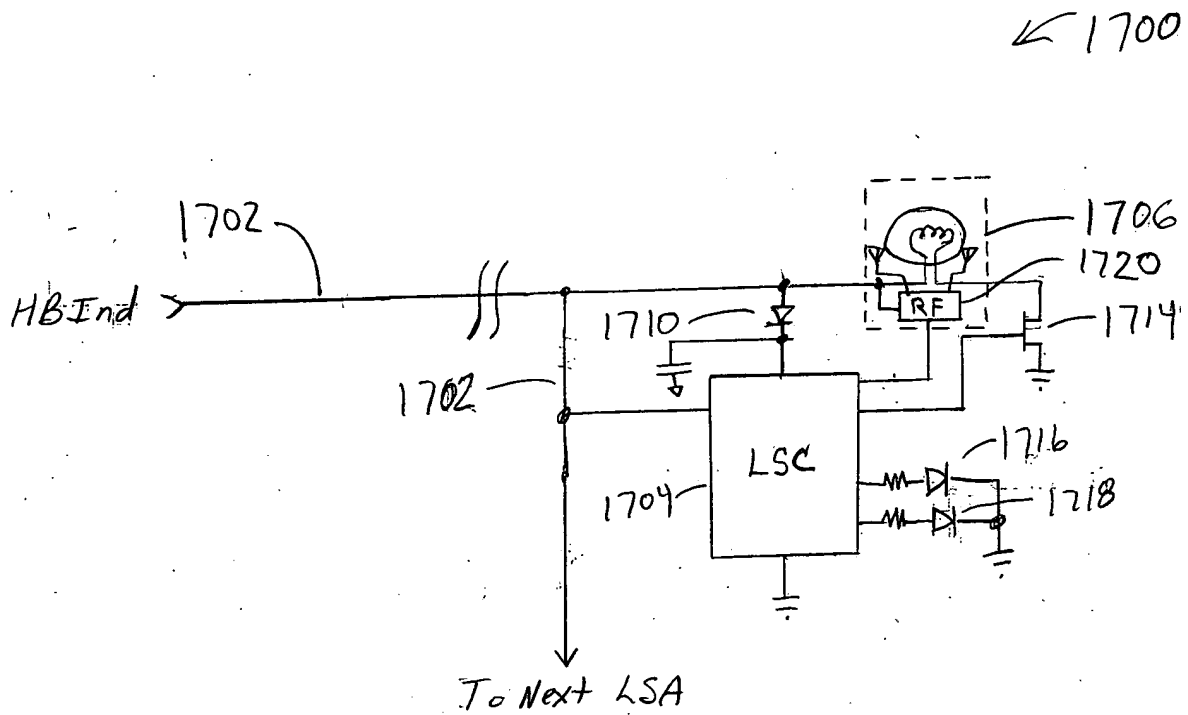


FIG. 36

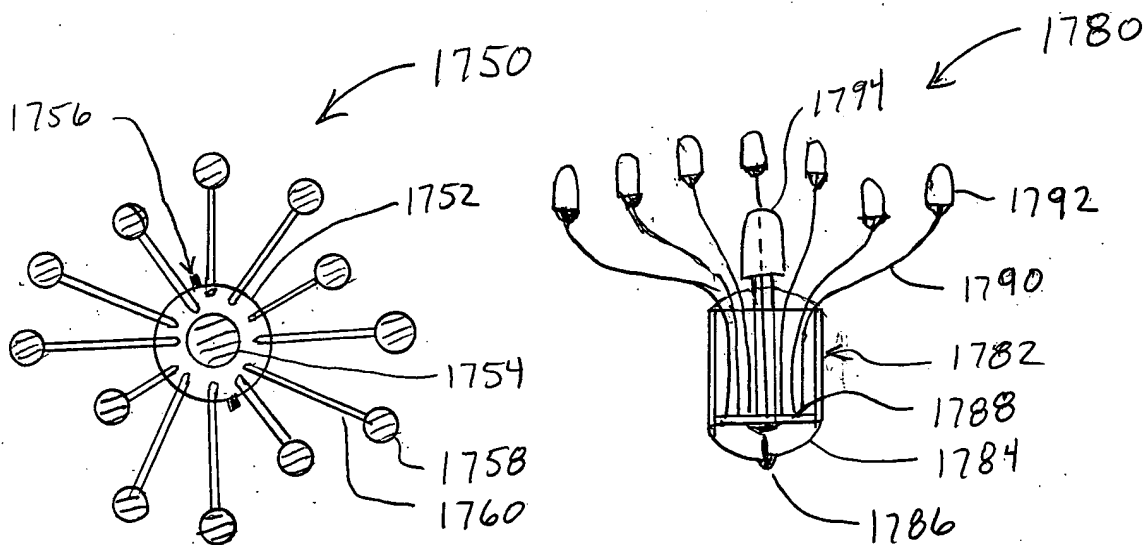


FIG. 37

FIG. 38



FIG. 41

